Docket No.: FQ5-562
KAT.027

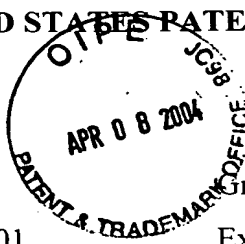# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Satoshi Kamiya, et al.

Serial No.: 09/966,099   Group Art Unit: 2661

Filed: October 1, 2001   Examiner: Unknown

For: TWO-DIMENSIONAL PIPELINED SCHEDULING TECHNIQUE

Honorable Commissioner of Patents
Alexandria, VA 22313-1450

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INFORMATION DISCLOSURE STATEMENT

Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicant's duty of disclosure under 37 CFR §1.56, applicant respectfully brings the following documents, cited in the European Search Report and listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. Copies of the listed documents are provided herewith for the convenience of the Examiner.

This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicant is aware.

I hereby certify that each item of information contained in this Information Disclosure Statement was the first citation of that item by a foreign patent office in a counterpart foreign application, which occurred not more than three months prior to the filing of this statement.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

Please charge any deficiencies in fees and credit any overpayment of fees to Attorney's Deposit Account No. 50-0481.

Respectfully submitted,

Sean M. McGinn
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Date: 4 8 04
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Docket Number (Optional)	Application Number
FQ5-502	09/966,099

Applicant(s)
Satoshi Kamiya, et al.

Filing Date	Group Art Unit
October 1, 2001	2661

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

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FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation YES	Translation NO

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

		European Search Report dated February 19, 2004.
		Yuval Tamir ,et al., "Symmetric Crossbar Arbiters for VLSI Communication Switches", IEEE Transactions on Parallel and Distributed Systems, Vol. 4, No. 1, 1993, pp. 13-27, XP000381322.

EXAMINER	DATE CONSIDERED

	INFORMATION DISCLOSURE CITATION *(Use several sheets if necessary)*	Docket Number (Optional) FQ5-502	Application Number 09/966,099
		Applicant(s) Satoshi Kamiya, et al.	
		Filing Date October 1, 2001	Group Art Unit 2661

*EXAMINER INITIAL	OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*
	Hsin-Chou Chi et al., "Starvation Prevention for Arbiters of Crossbars with Multi-Queue Input Buffers", February 28, 2004, pp. 292-297, XP000479405.
	James Hurt et al., "Design and Implementation of High-Speed Symmetric Crossbar Schedulers", June 6, 1999, Vol. 3, pp. 1478-1483, XP000903619.

EXAMINER	DATE CONSIDERED

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609: Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

P09B/REV04